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Application No.: 10/740,465  
Art Unit 2629Attorney Docket No. 0465-1062P  
Reply to Office Action dated April 9, 2008  
Page 2**Amendments to the Claims**

1. (Previously Presented) A driving method for displaying a normal mode signal in a wide mode liquid crystal display (LCD) device, for displaying an analog video signal having a horizontal back porch input to the wide mode LCD device as a normal mode, the method comprising:

outputting a source start pulse (SSP) signal;

latching pixel data for a black display by using a main clock signal having a first period synchronized to the SSP signal;

first skipping latch of the pixel data for the black display during a first transition period of the video signal by using a clock enable signal disabled at the first transition period of the video signal;

latching pixel data corresponding to a normal mode by using a modulated clock signal having a second period that is longer than the first period, and outputting the latched pixel data; and

second skipping latch of the pixel data corresponding to a normal mode during a second transition period of the video signal by using the clock enable signal disabled at the second transition period of the video signal.

2. (Original) The driving method of claim 1, wherein in the outputting step, the SSP signal is output after a predetermined time period from a horizontal start pulse (HSP).

3. (Original) The driving method of claim 2, wherein the predetermined time period is 1.048 $\mu$ s.

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4. (Original) The driving method of claim 2, wherein in the outputting step, the SSP signal is output after a certain time period from a rising edge of the HSP.

5. (Original) The driving method of claim 1, wherein in the first skipping step, the data latch corresponding to 42 to 45 pixels is skipped.

6. (Original) The driving method of claim 1, wherein in the second skipping step, the data latch corresponding to 52 pixels is skipped.

7. (Previously Presented) The driving method of claim 1, wherein the first period of the clock signal lasts from a start of the SSP signal to an end of the horizontal back porch.

8. (Original) The driving method of claim 1, wherein at least one of the first and second skipping steps is performed by disabling an enable clock signal.

9. (Original) The driving method of claim 1, wherein the long period of the clock signal corresponds to  $50.3\mu\text{s}$ .

10-21. (Canceled)